

Memory Address(DEC)	HEX	Register Name	Read/Write	Non-Volatile	Auto reboot after change	Factory Default	min	max	Description
1	0x01	SBEEP	R/W	-	-	0x00			<p>Simple Beep</p> <p>The buzzer sounds according to the pattern set in this register. ex. If the frequency is 1kHz, the period is 1s, and the number of repetitions is 2times: 1kHz beeps for 1s -> silence for 1s -> 1kHz beeps for 1s -> silence for 1s. Writing 0x00 while the buzzer is sounding will stop the buzzer. The buzzer is automatically reset to 0x00 after sounding. Cannot be set if CBEEP is other than 0x00. Bit [7-5]: Frequency. 0b000: Disable, 0x001: 100Hz, 0b010: 200Hz, 0b011: 500Hz, 0b100: 1kHz, 0b101: 2kHz, 0b110: 4kHz, 0b111: 8kHz Bit [4-2]: Period. 0b000: disabled, 0b001: 50ms, 0b010: 100ms, 0b011: 200ms, 0b100: 500ms, 0b101: 1s, 0b110: 2s, 0b111: permanent Bit [1-0]: iteration count. 0b00: disabled, 0b01: 1time, 0b10: 2times, 0b11: 3times</p>
2	0x02	CBEEP	R/W	-	-	0x00			<p>Custom Beep</p> <p>The buzzer sounds with the pattern (max 61 tones) set in the FREQ0 to PERIOD60 registers. Writing 0x00 while the buzzer is sounding will stop the buzzer. The buzzer is automatically reset to 0x00 after sounding. Cannot be set if SBEEP is other than 0x00. Bit [7-4]: Reserved Bit [3-0]: Ringing status/repetition count. 0b000: Stop ringing, 0b0001 to 0b1110: Start ringing and repeat pattern for the set number of times, 0b1111: Repeat permanently</p>
4 - 5	0x04 - 0x05	FREQ0	R/W	○	○	65535	0	8000	<p>Frequency of Tone 0 [Hz]</p> <p>If 0 to 99 is set, the buzzer will be silent; if 8001 to 65535 is set, the buzzer will stop at that stage.</p>
6 - 7	0x06 - 0x07	PERIOD0	R/W	○	○	1000	0	65535	<p>Length of Tone 0 [ms]</p> <p>If set to 0, the tone is permanent.</p>
8 - 9	0x08 - 0x09	FREQ1	R/W	○	○	65535	0	8000	Frequency of Tone 1 [Hz]
10 - 11	0x0A - 0x0B	PERIOD1	R/W	○	○	1000	0	65535	Length of Tone 1 [ms]
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
244 - 245	0xF4 - 0xF5	FREQ60	R/W	○	○	65535	0	8000	Frequency of Tone 60 [Hz]
246 - 247	0xF6 - 0xF7	PERIOD60	R/W	○	○	1000	0	65535	Length of Tone 60 [ms]
250	0xFA	VMAJOR	R	-	-	[1,255]			Firmware major version
251	0xFB	VMINOR	R	-	-	[0,255]			Firmware minor version
252	0xFC	VPATCH	R	-	-	[0,255]			Firmware patch version
253	0xFD	SYSRBT	W	-	○	-	-	-	<p>System Reboot</p> <p>If 0xFF is written to the register, reboot system.</p>
254	0xFE	I2CADDR	R/W	○	○	0x16	0x08	0x77	I2C device address
255	0xFF	INIT	W	-	○	-	-	-	<p>System initialization</p> <p>If 0xFF is written to the register, initialize all register values to factory default value then reboot. CAUTION: The I2C device address also returns to the factory default value, so next time you need to operate with the default I2C address.</p>

*1. Byte order is little endian.